WEST Search History

Hide Items | Restore | Clear | Cancel

DATE: Monday, July 17, 2006

Hide?	<u>Set</u> <u>Name</u>	Query	<u>Hit</u> Count		
DB=PGPB,USPT; PLUR=YES; OP=ADJ					
	L10	L9 same l4	350		
	L9	12 near12 17	1630		
	L8	L6 same L7	1582		
	L7	(count\$3 or number\$1 or size\$1) near5 (increment\$3 or increas\$3 or decrement\$3 or decreas\$3 or add\$3 or subtract\$3)	878466		
	L6	L5 same (increment\$3 or increas\$3 or decrement\$3 or decreas\$3 or add\$3 or subtract\$3)	3022		
	L5	L3 same L4	11304		
	L4	processor or cpu or microprocessor	801852		
	L3	L2 same (count\$3 or number\$1 or size\$1)	43833		
	L2	L1 near3 (buffer\$1 or queue\$1 or register\$1 or container\$1 or post\$3)	117129		
	Ll	store or write or writeback	873251		

END OF SEARCH HISTORY

WEST Search History

Hide Items Restore Clear Cancel

DATE: Monday, July 17, 2006

Hide?	Hit Count		
	DB=PC	GPB,USPT; PLUR=YES; OP=ADJ	
	L5	L2 near7 count\$3 near7 (capacity or full\$4 or empty)	63
	L4	L3 same (capacity or full\$4 or empty)	928
	L3	L2 same count\$3	5019
	L2	L1 near2 (buffer\$1 or queue\$1 or container\$1)	54248
	L1	store or write or writeback	873251

END OF SEARCH HISTORY

WEST Search History

Hide Items	Restore	Clear.	Cancel	

DATE: Monday, July 17, 2006

Hide?	Hit Count			
DB=PGPB,USPT; PLUR=YES; OP=ADJ				
	L10	12 near12 18	42	
	L9	12 same L8	258	
	L8	(processor or cpu or microprocessor) near5 count\$3	35259	
	L7	(processor or cpu) same 13	964	
	L6	entry tracking logic	1	
	L5	L2 near7 count\$3 near7 (capacity or full\$4 or empty)	63	
	L4	L3 same (capacity or full\$4 or empty)	928	
	L3	L2 same count\$3	5019	
	. L2	L1 near2 (buffer\$1 or queue\$1 or container\$1)	54248	
	Ll	store or write or writeback	873251	

END OF SEARCH HISTORY